

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**APPLICANT:** Gormley et al. **GROUP:** Unknown

**SERIAL NO:** Unknown **EXAMINER:** Unknown  
(CON appln. of ser. no. 10/194,392)

**FILED:** Herewith

**FOR: SEMICONDUCTOR WAFER COMPRISING MICRO-MACHINED  
COMPONENTS AND A METHOD FOR FABRICATING THE  
SEMICONDUCTOR**

Assistant Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**PRELIMINARY AMENDMENT**

Preliminary to examination, please amend the above-identified application as follows: